In the Claims:

Please cancel claims 28, 31 and 33-46.

Remaining claims are 25-27, 29, 30 and 32.

- 25. (Original) A method for fabricating a magnetic tunnel junction (MTJ) sensor for use in a magnetic read head having an air bearing surface (ABS), the method comprising the unordered steps of:
- (a) forming a MTJ stack with an active region disposed at the ABS and having two opposite sides each disposed generally orthogonally to the ABS, including the unordered steps of:
 - (a.1) forming an antiferromagnetic (AFM) layer,
 - (a.2) forming a pinned layer of ferromagnetic (FM) material in contact with the AFM layer,
 - (a.3) forming a free layer of FM material,
 - (a.4) forming a tunnel junction layer of electrically nonconductive material disposed between the pinned layer and the free layer, and
 - (a.5) removing all material outside of the active region from the AFM layer, the pinned layer, and the tunnel junction layer to define the two opposite sides of the active region; and
- (b) forming a longitudinal bias layer outside of the active region in contact with the free layer for biasing the magnetic moment of the free layer in substantially a predetermined direction in the absence of an external magnetic field.
 - 26. (Original) The method of claim 25 further comprising the step of:
- (c) forming an insulating layer of electrically nonconductive material on and in contact with the free layer outside of the active region and in abutting contact with the two opposite sides of the active region.
- 27. (Original) The method of claim 26 wherein the longitudinal bias layer is disposed without contacting the active region.

- 28. (Cancelled)
- 29. (Original) The method of claim 27 wherein the longitudinal bias layer comprises an AFM material.
- 30. (Original) The method of claim 25 wherein the longitudinal bias layer is disposed without contacting the active region.
 - 31. (Cancelled)
- 32. (Original) The method of claim 30 wherein the longitudinal bias layer comprises an AFM material.

33-46 (Cancelled)

Respectfully submitted,

Wipul Jayasekara

Date: $\frac{9/30/65}{}$ By

Matthew S. Zises (#47,246) Attorney for Applicant(s) Hitachi Global Storage Technologies

Intellectual Property Law

5600 Cottle Road (NHGB/142) San Jose, CA 95193

(408) 717-6759